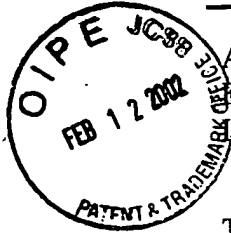


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Application No.: 09/596,863  
Filed: June 19, 2000  
Inventor(s):  
Novak, Istvan

Title: Bypass Capacitor Methods  
For Achieving A Desired  
Value Of Electrical  
Impedance Between Parallel  
Planar Conductors Of An  
Electrical Power  
Distribution Structure And  
Associated Electrical Power  
Distribution Structures

Examiner: Bettendorf, J.  
Group/Art Unit: 2817  
Atty. Dkt. No: 5181-62800

I hereby certify that this correspondence is being deposited with  
the United States Postal Service with sufficient postage as first  
class mail in an envelope addressed to Commissioner for  
Patents, Washington, DC 20231, on the date indicated below.

B. Noel Kivlin

Printed Name

Signature

January 10, 2002

Date

**REQUEST FOR APPROVAL OF DRAWING CHANGES  
UNDER 37 C.F.R. §1.121(d)**

Commissioner for Patents  
Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. §1.121(d), Applicant respectfully requests that the changes  
shown in red to the attached drawing (Figure 11) be approved by the Examiner in the  
above-identified application.

Respectfully submitted,

B. Noel Kivlin  
Reg. No. 33,929  
ATTORNEY FOR APPLICANT(S)

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Date: January 10, 2002

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